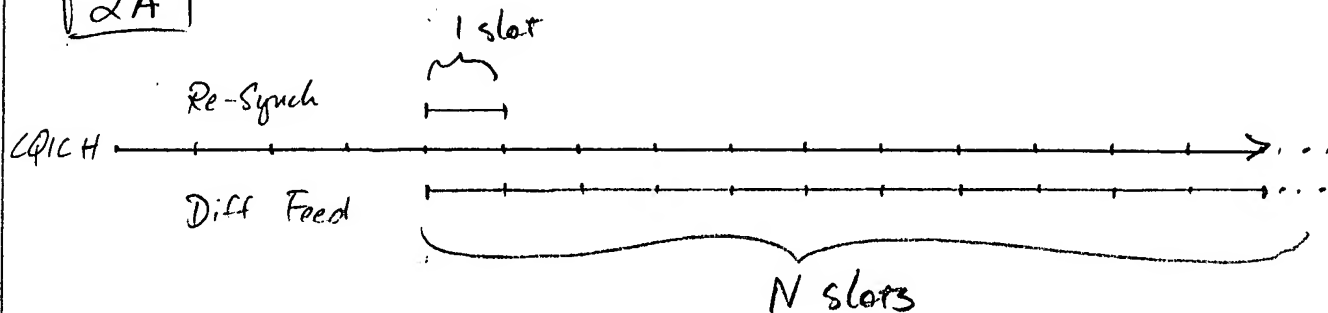


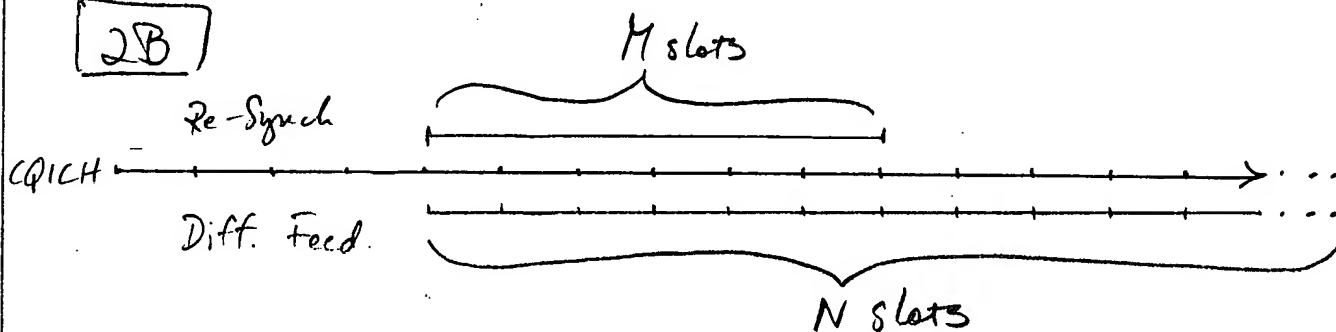
FIG. 1



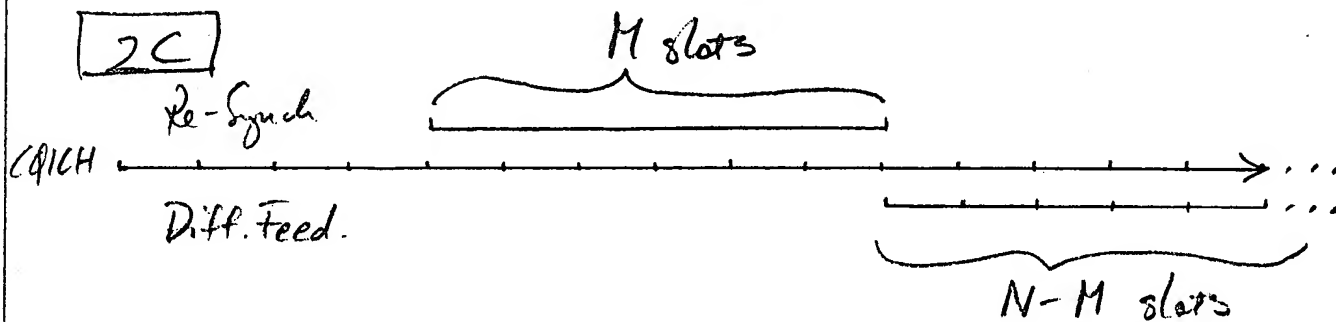
2A



2B



2C





206T20" B22505

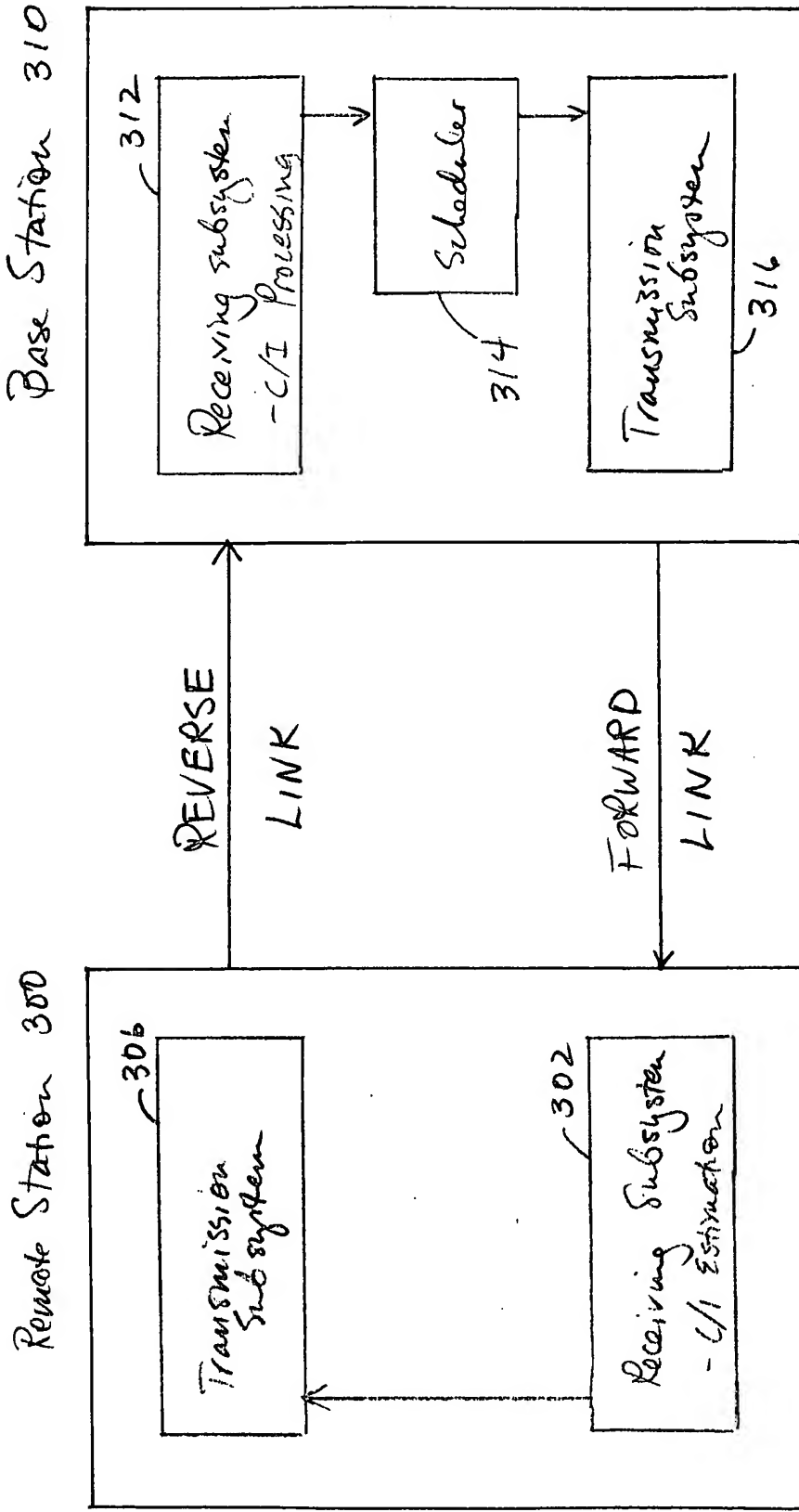
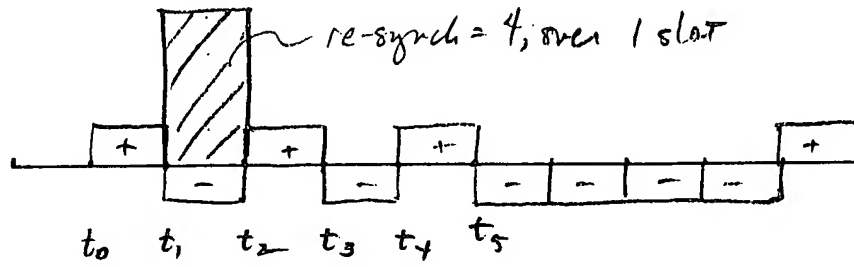
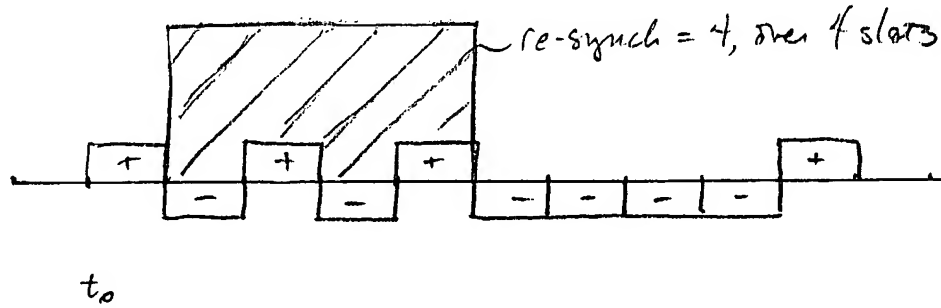


FIG. 3

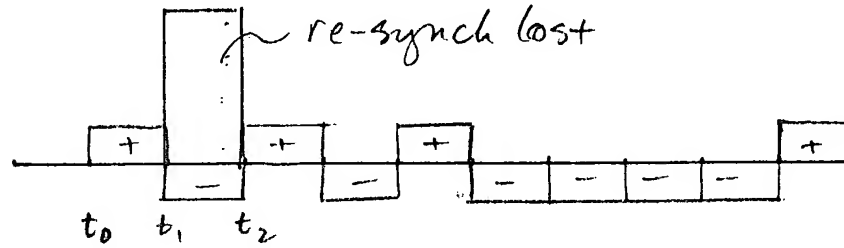
4A



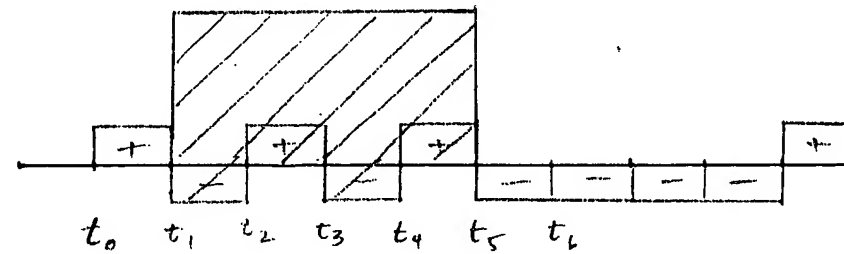
4B



4C



4D



At  $t_0$ , register contents = 10 dB  
+, -  $\Rightarrow$  incremental beats

4A	4B	4C	4D
10	10	10	10
11	11	11	11
4	11	Re-Synch OK	Add
5	11	Re-Synch Los?	Acc.+ Add
4	11	4	10
5	11	5	11
4	11	4	10
5	4	5	5 *
4	3	4	4

\* Note that 1<sup>st</sup> parallel! beat is discarded for "accumulate & add"

Fig. 4E

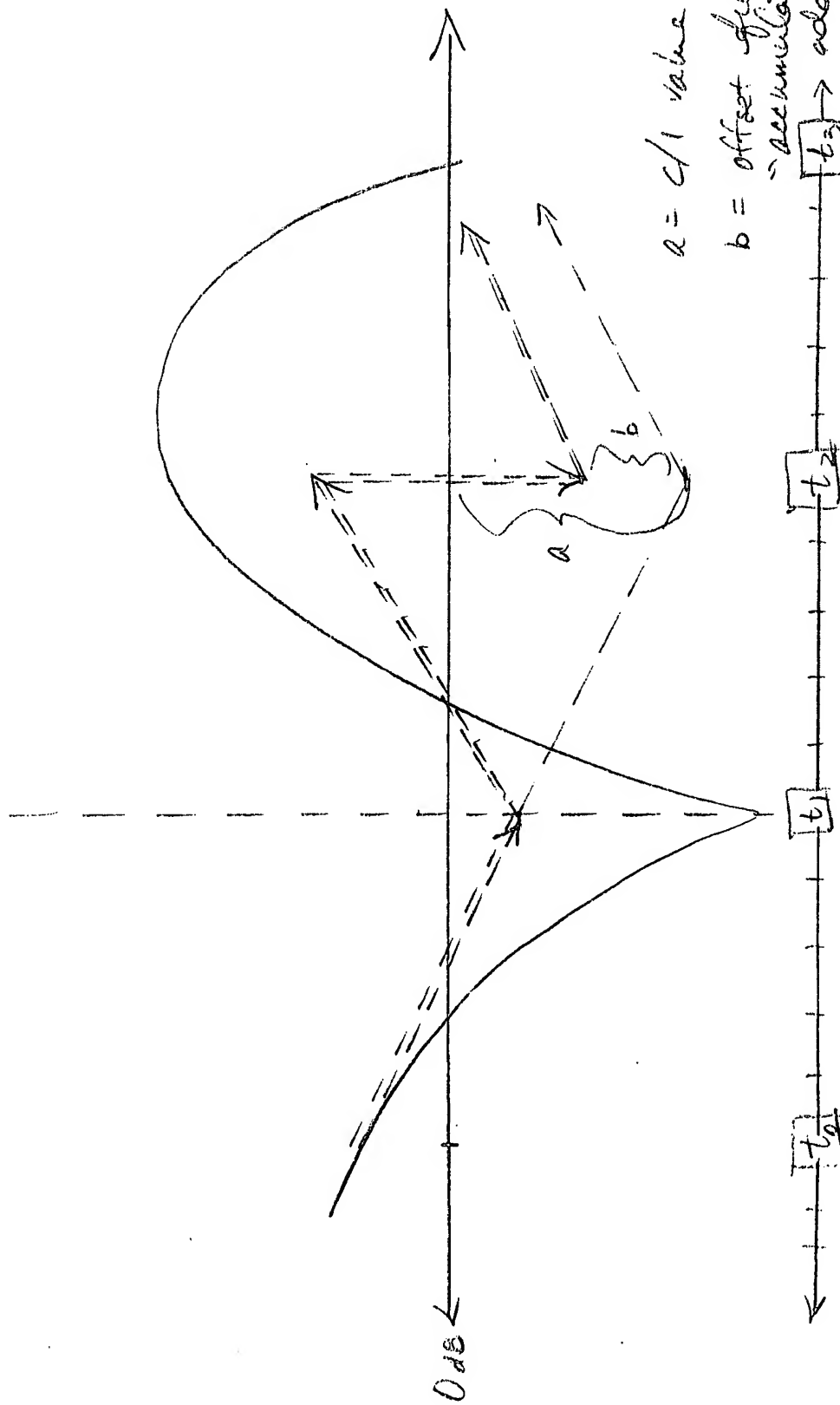


FIG. 5  
(not to scale)

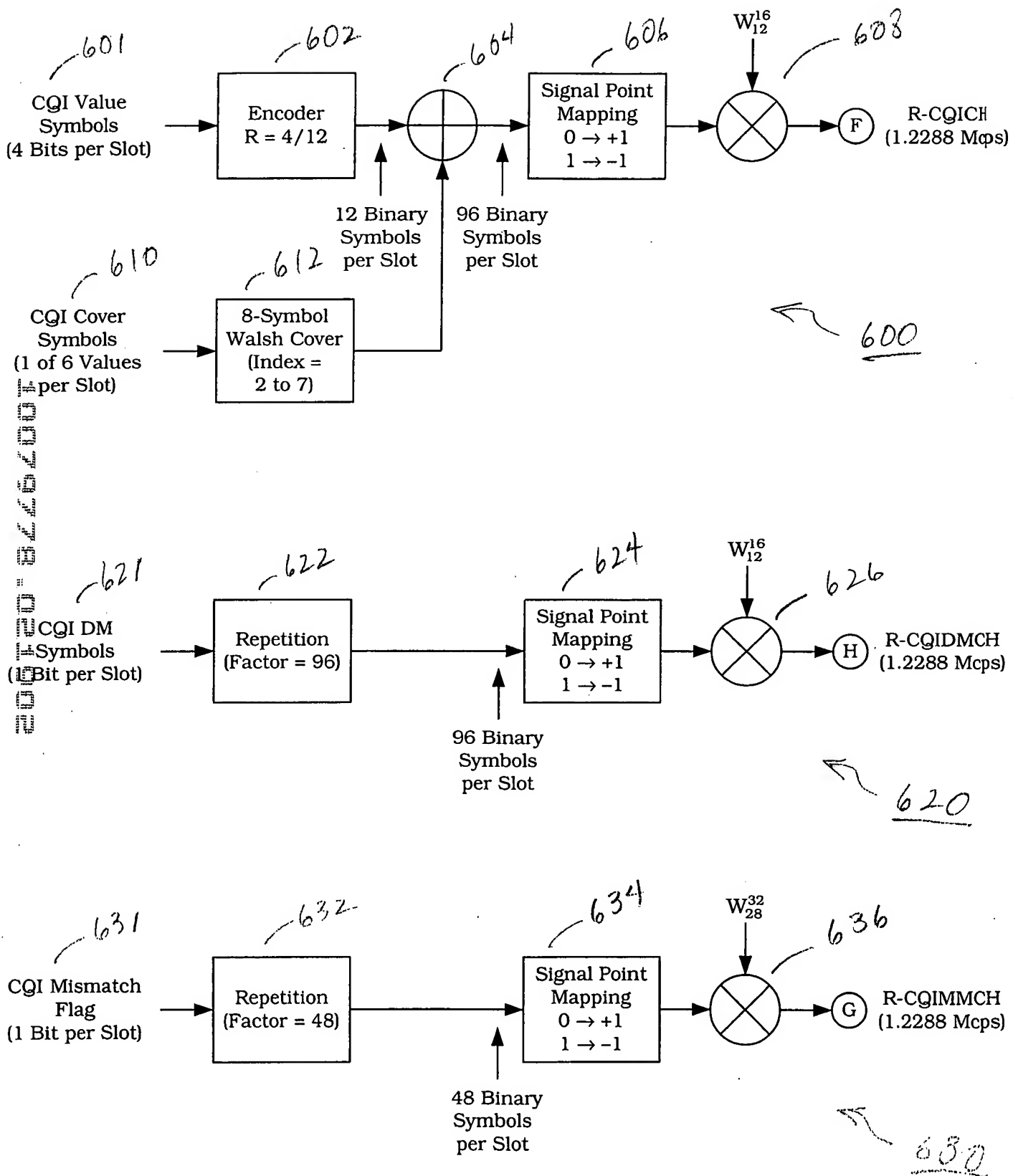


FIG. 6A

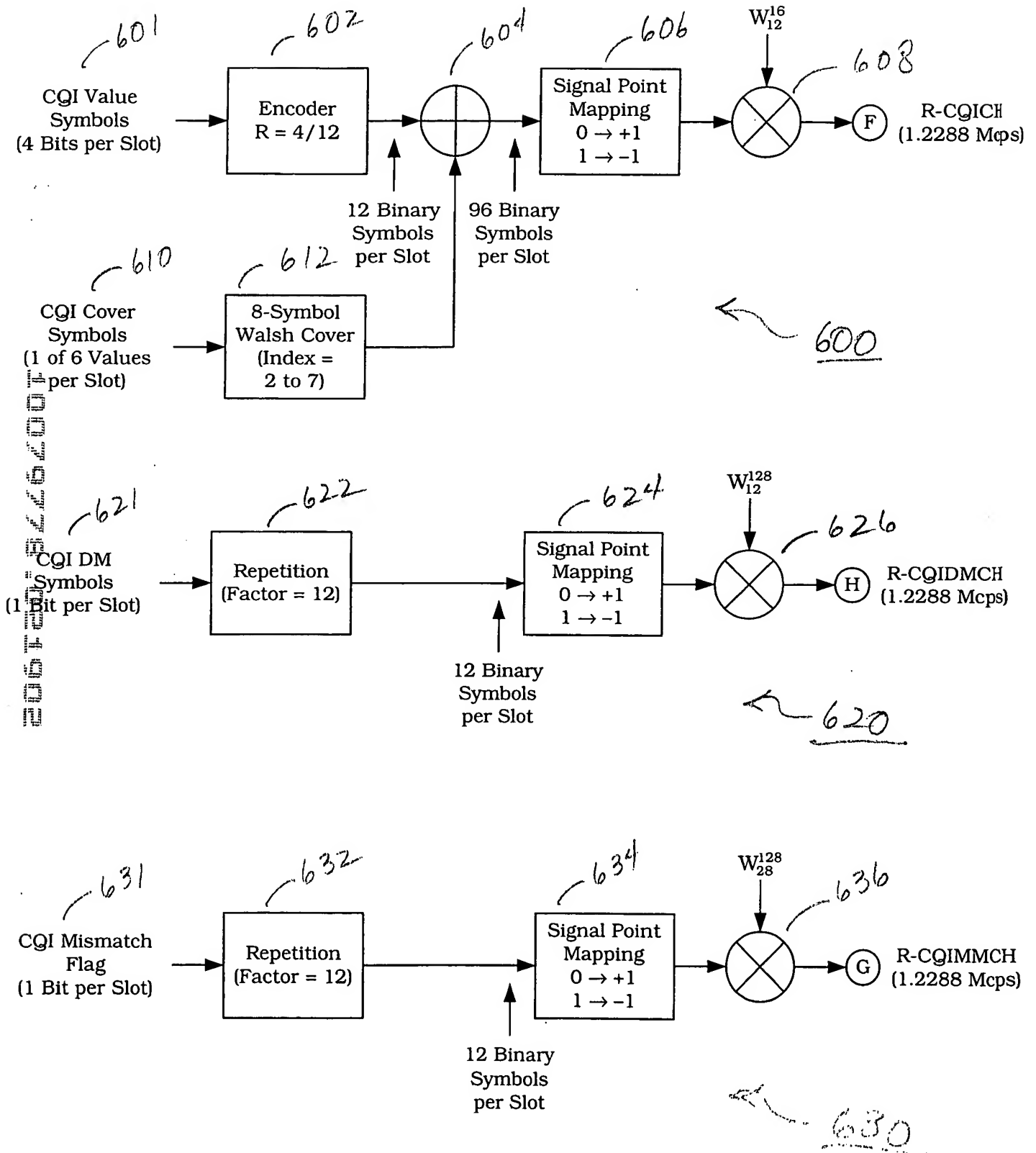
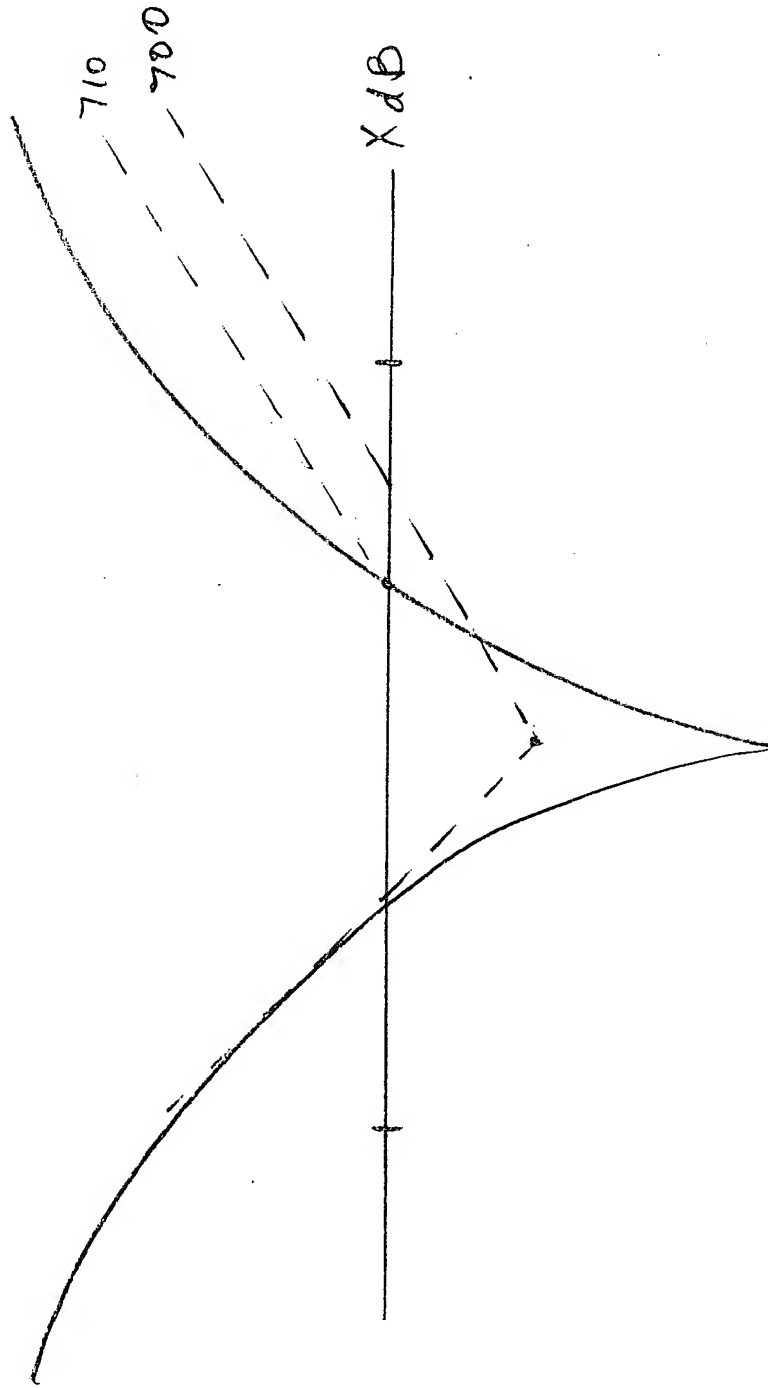


FIG. 6B





Re-synch tx over  $M$  slots

FIG. 7  
(not to scale)